

FIG. 1
(Prior Art)

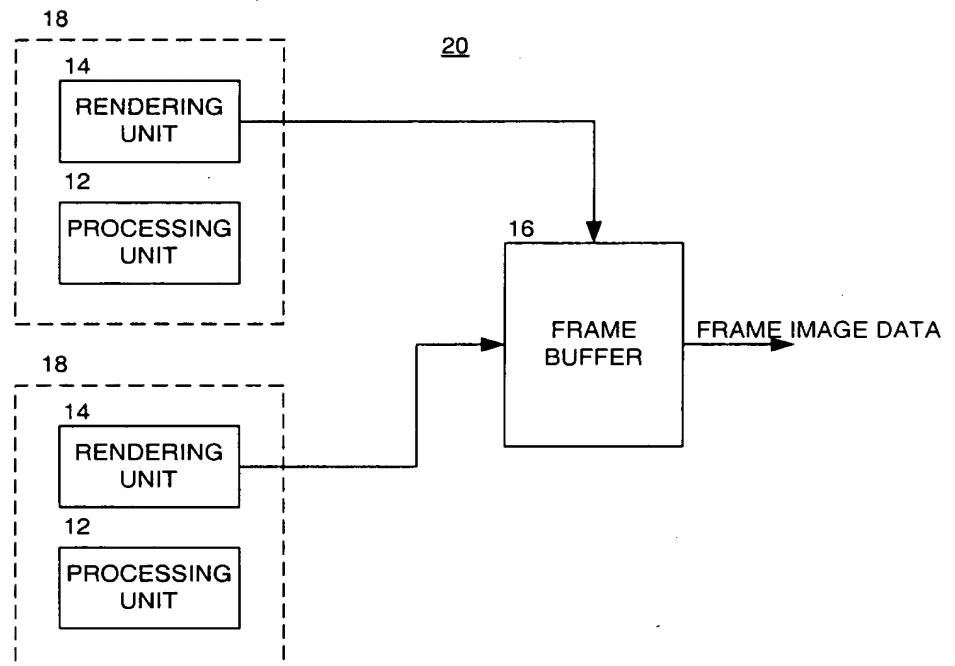
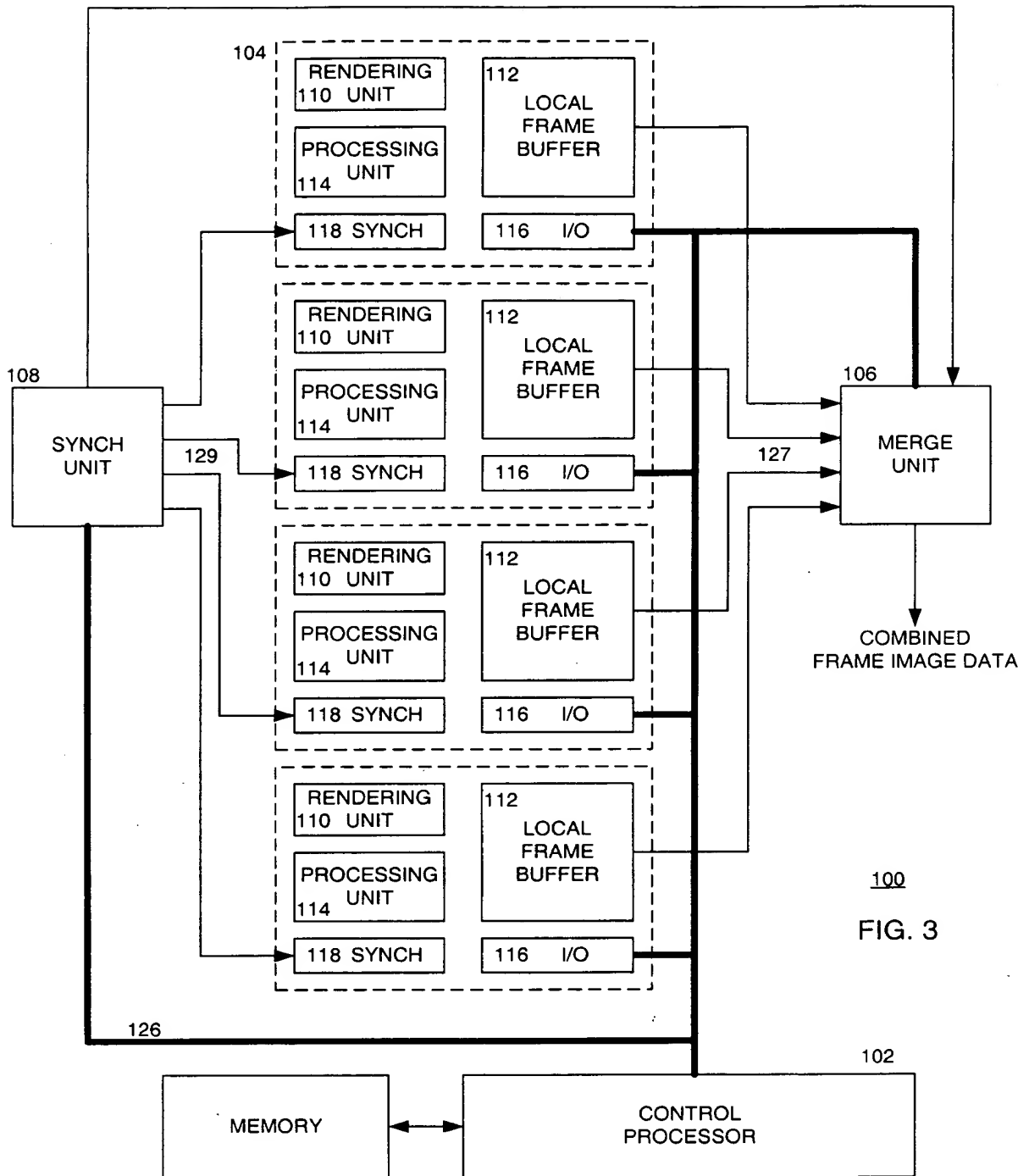


FIG. 2
(Prior Art)



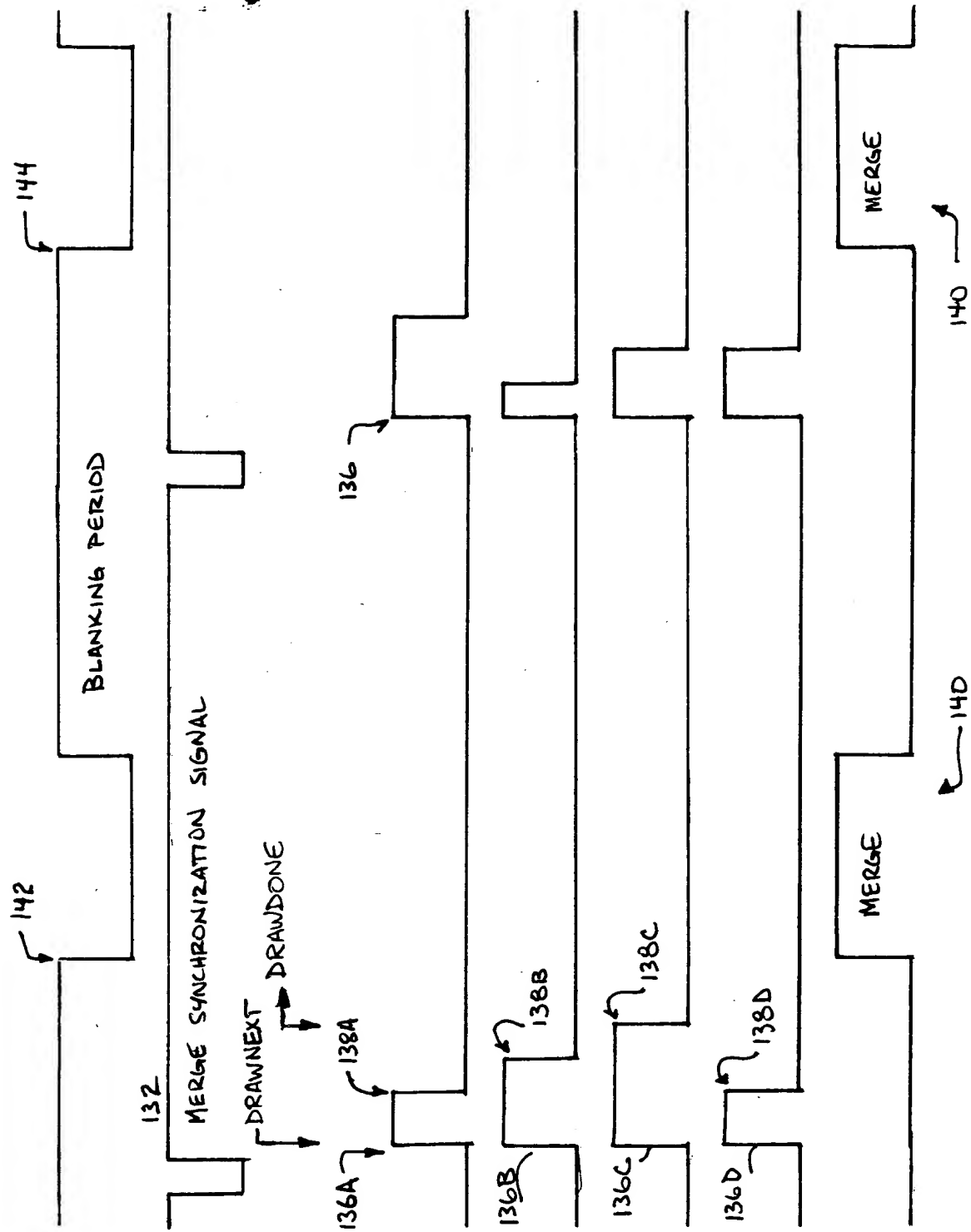


Fig. 4

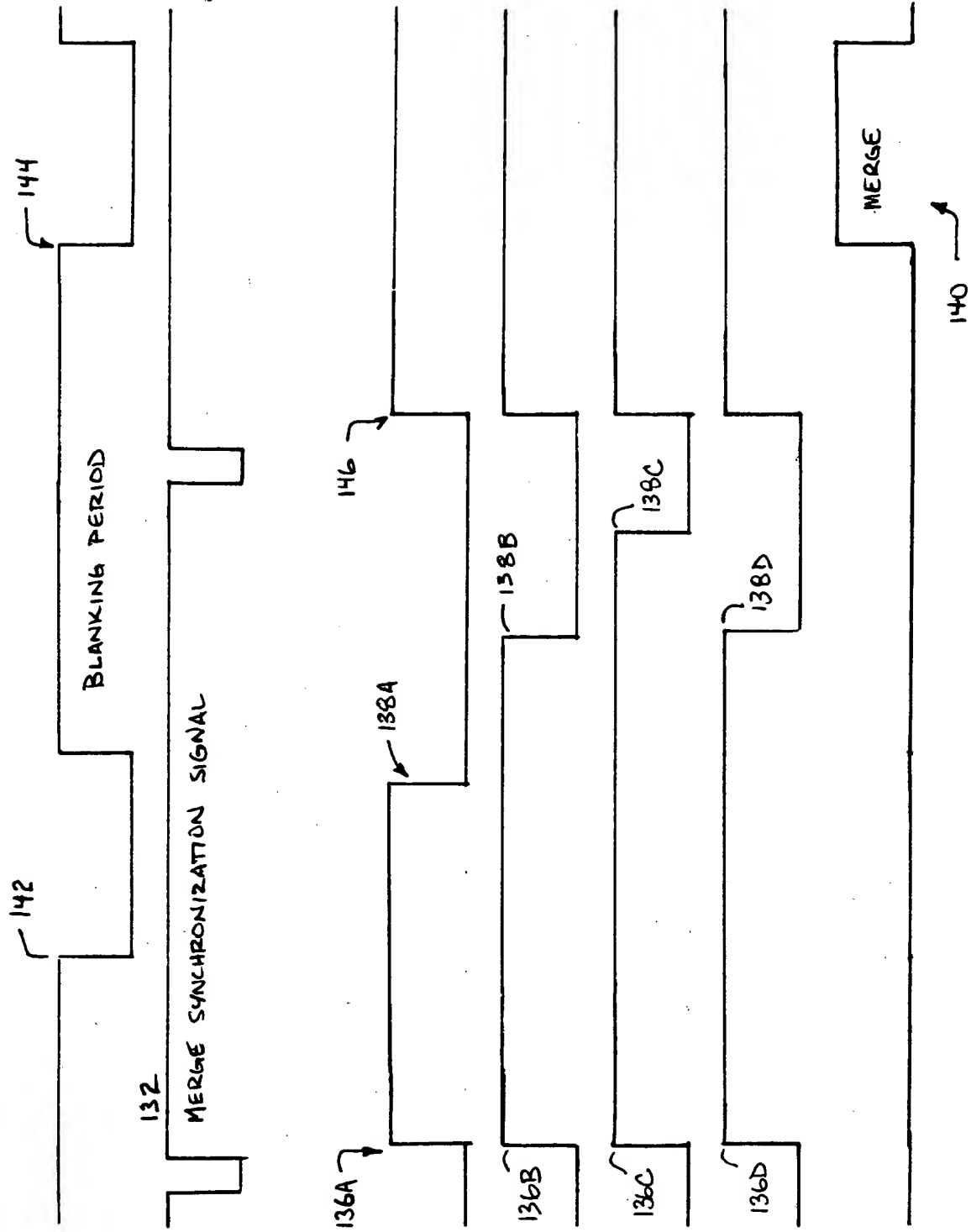


Fig. 5

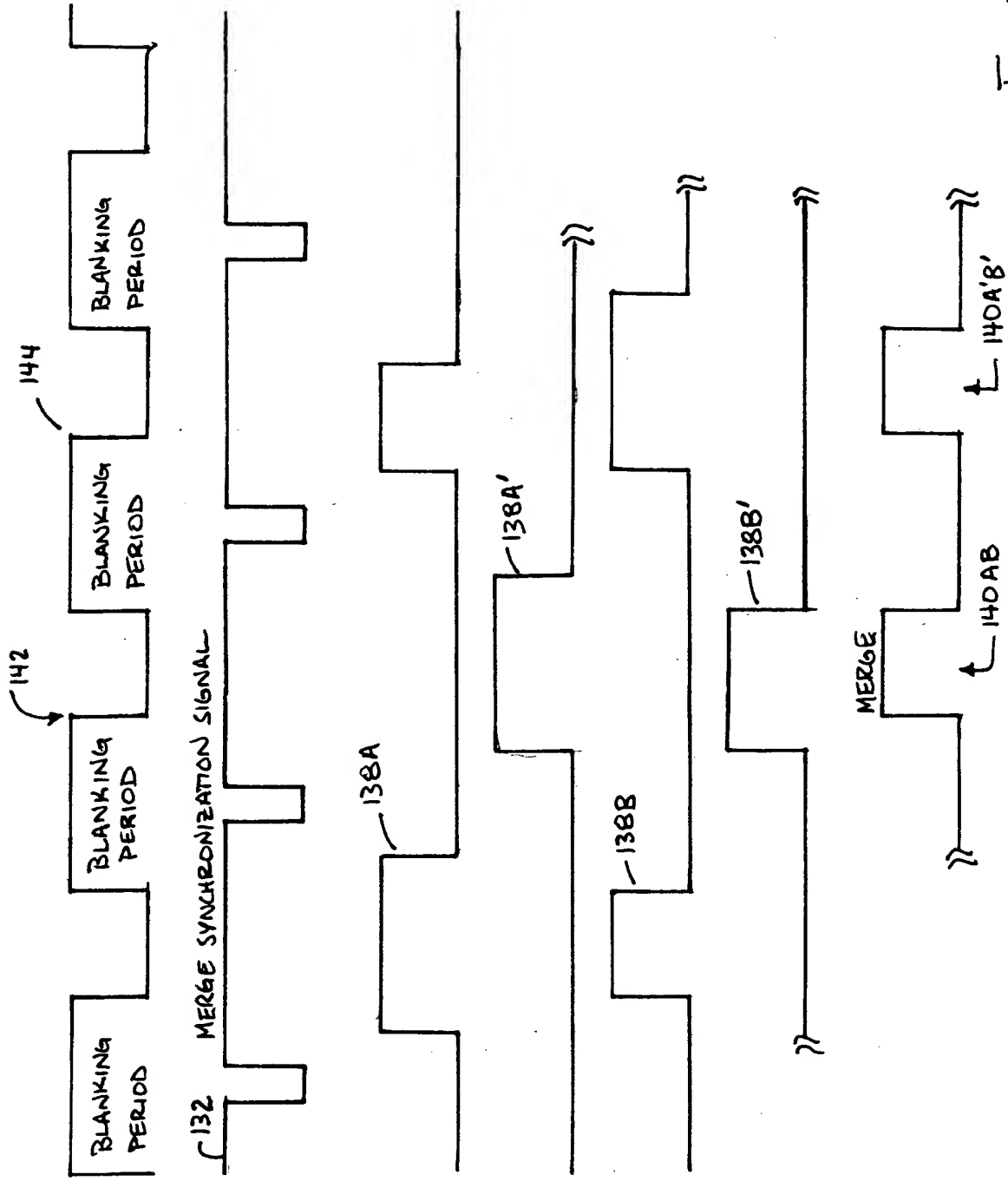


FIG. 6A

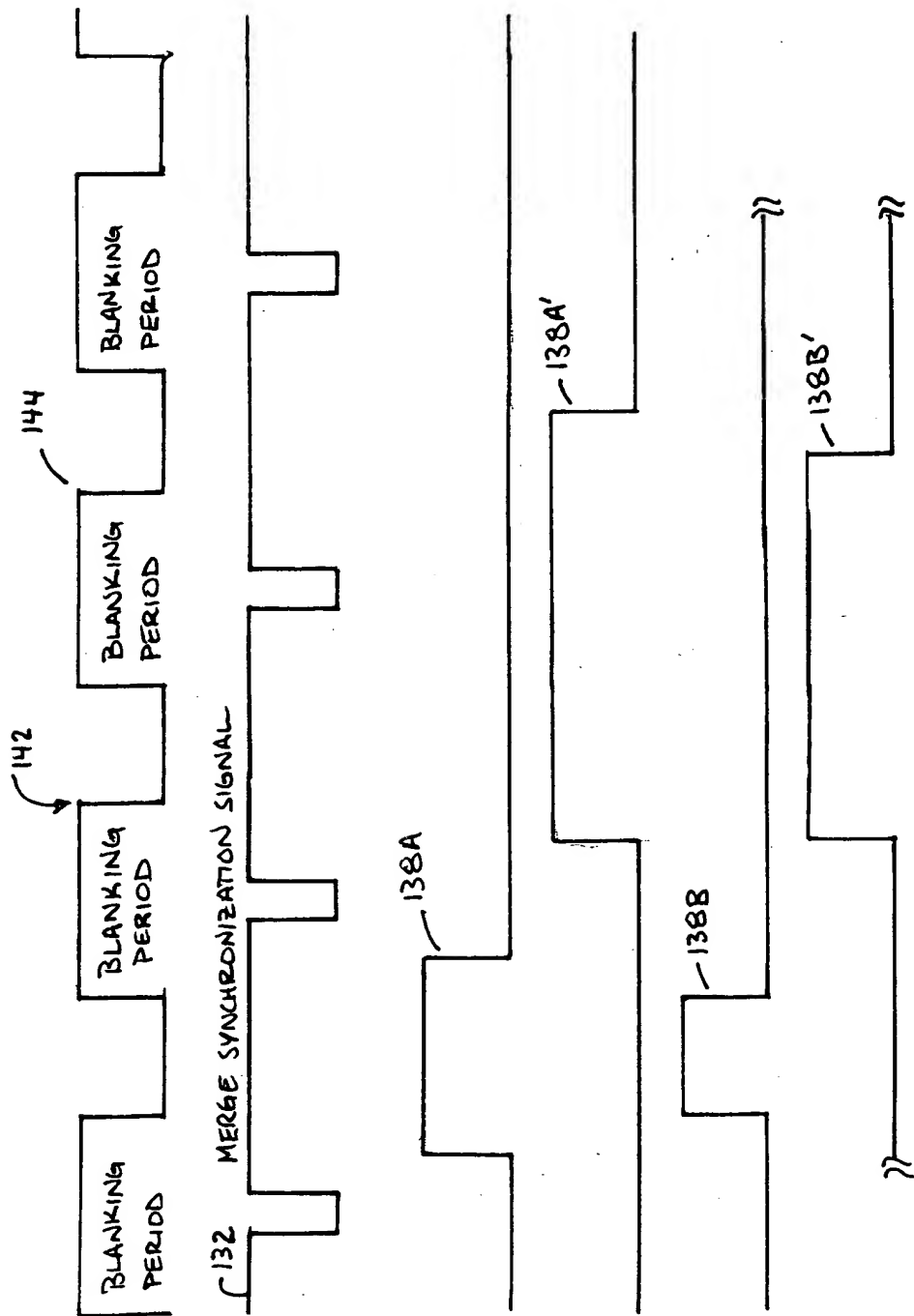


FIG. 6B

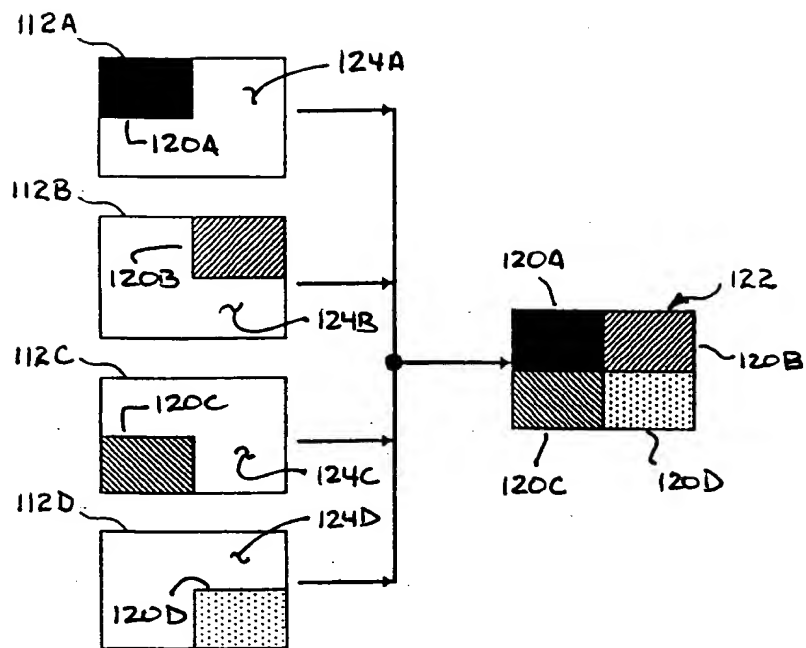


FIG. 7A

A block diagram illustrating a parallel processing system. On the left, four rectangular blocks are arranged vertically, labeled 112A, 112B, 112C, and 112D from top to bottom. Block 112A is filled with diagonal hatching. Block 112B is also filled with diagonal hatching. Block 112C is empty (white). Block 112D is filled with a dotted pattern. Arrows from each of these four blocks point to a central vertical line. A dot is located on this vertical line, from which an arrow points to a final rectangular block on the right, labeled 122. This final block is filled with a cross-hatch pattern.

FIG. 7B

00045701 032304

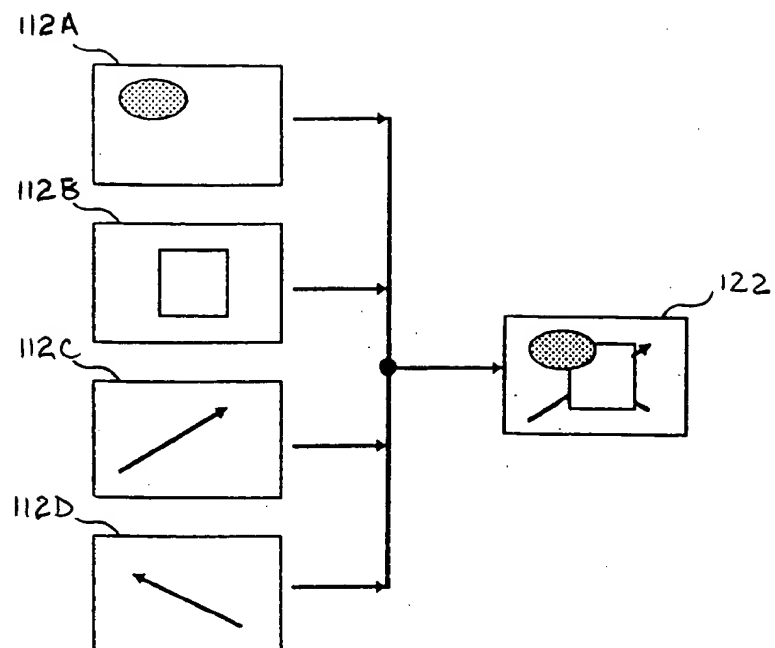


FIG. 7C

FIG. 7D

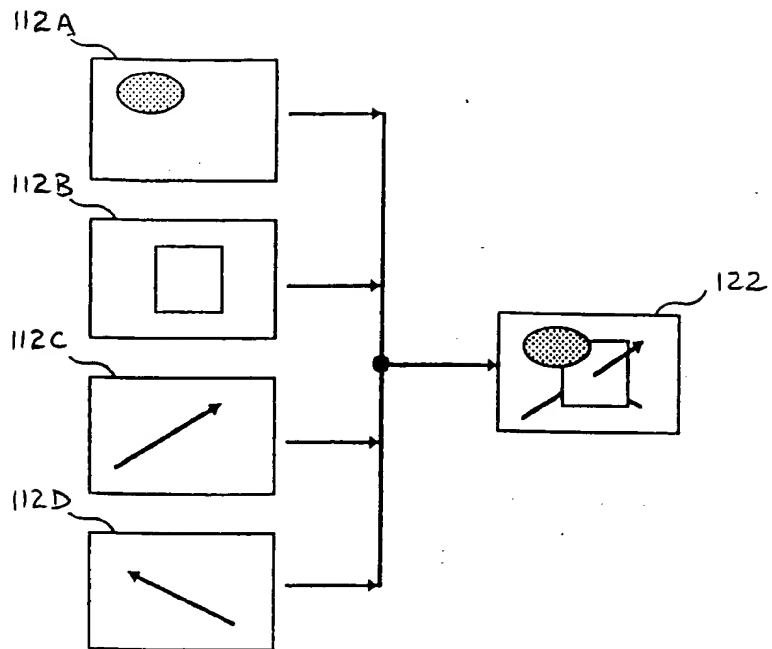


FIG. 7D

FIG. 7E

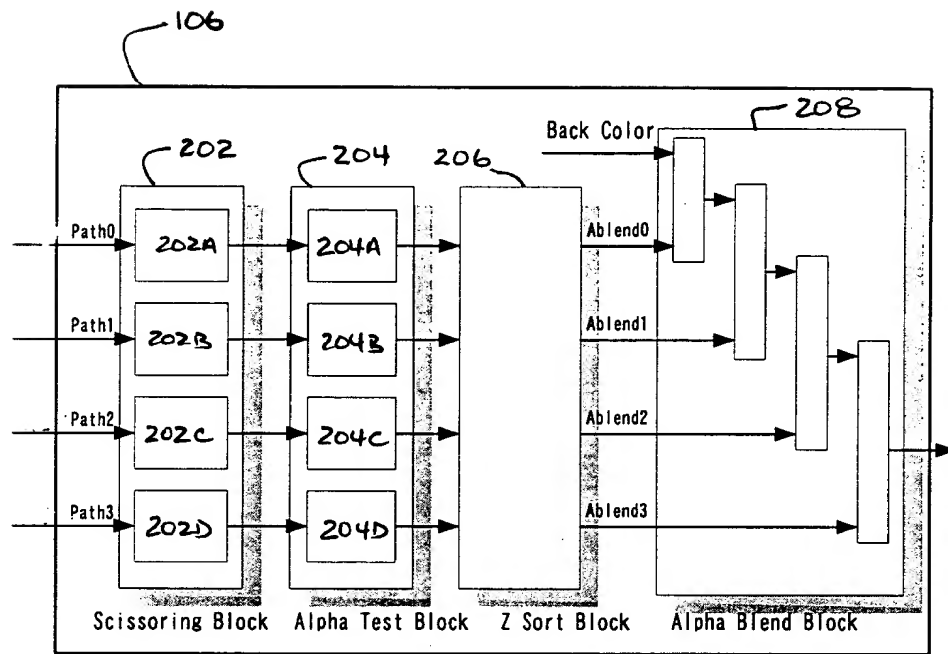


Fig. 8.

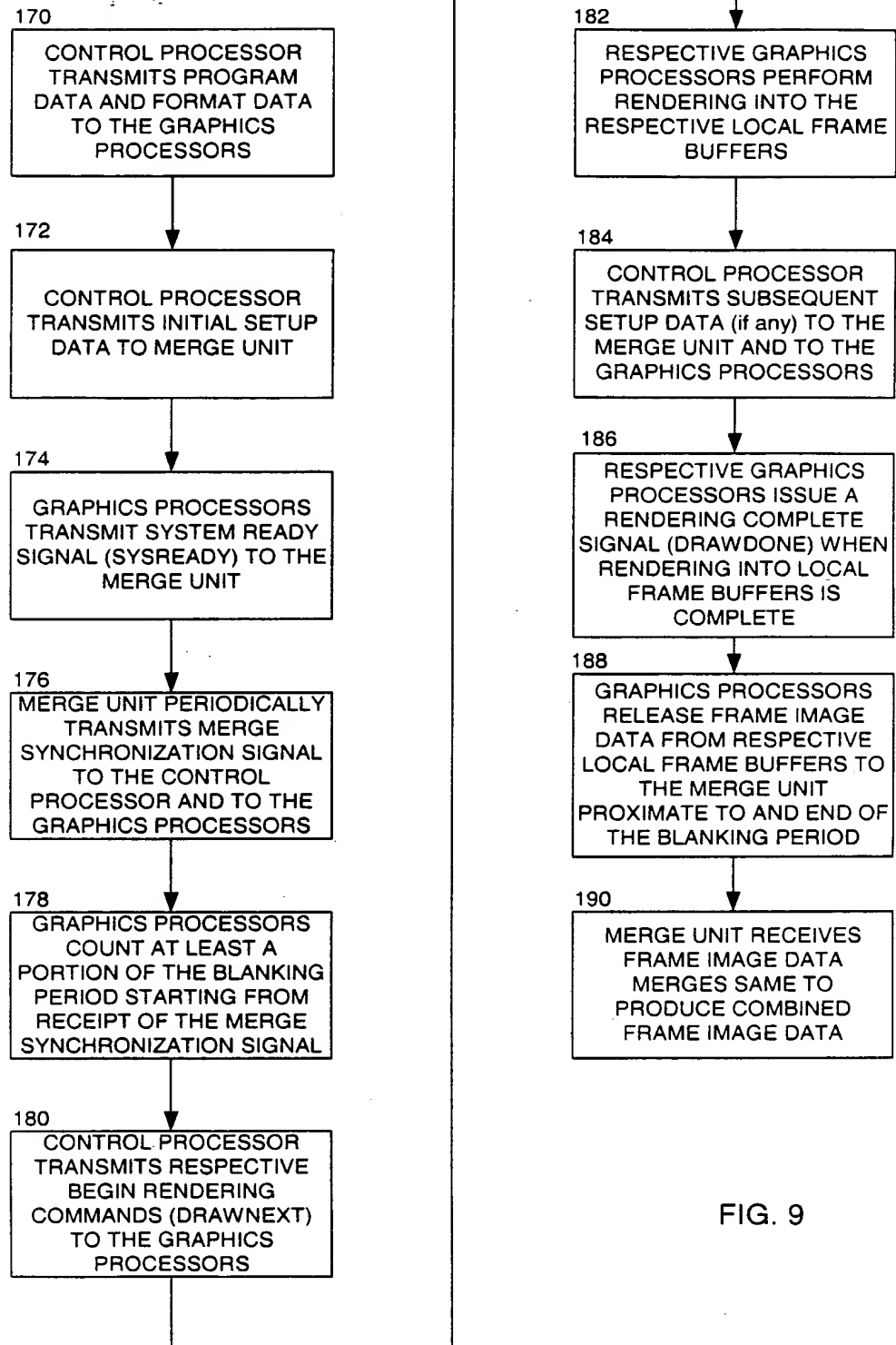


FIG. 9

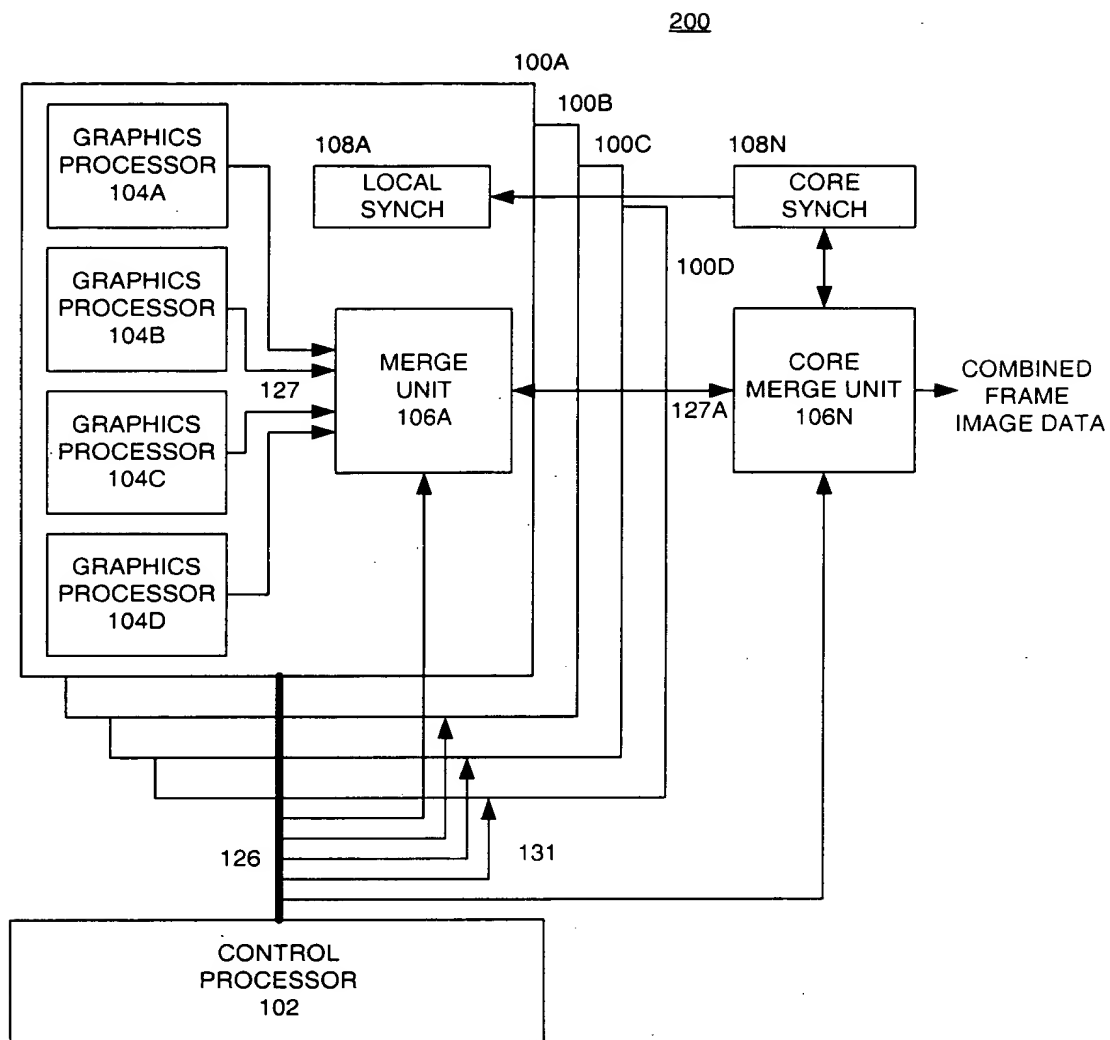


FIG. 10

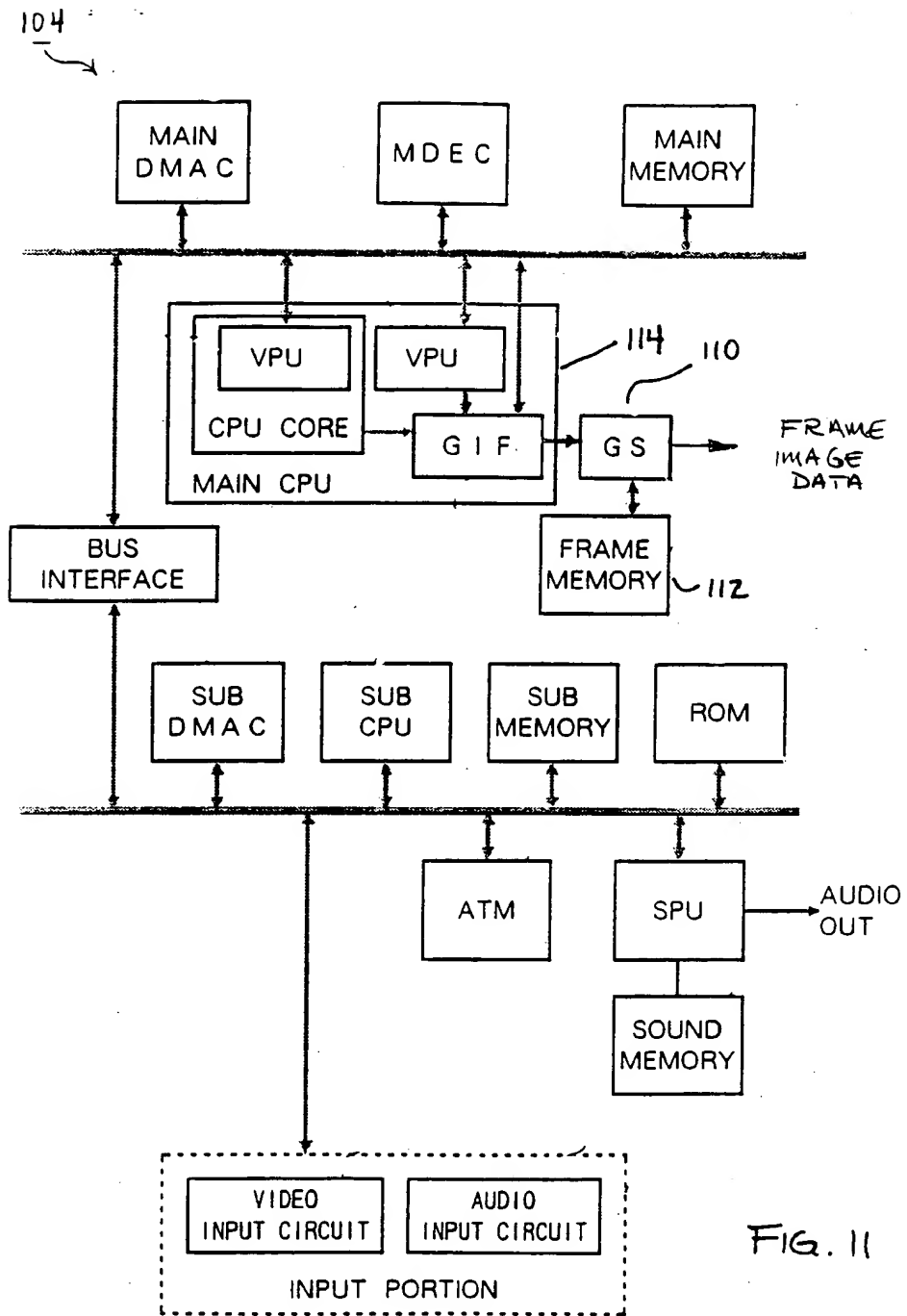


FIG. 11

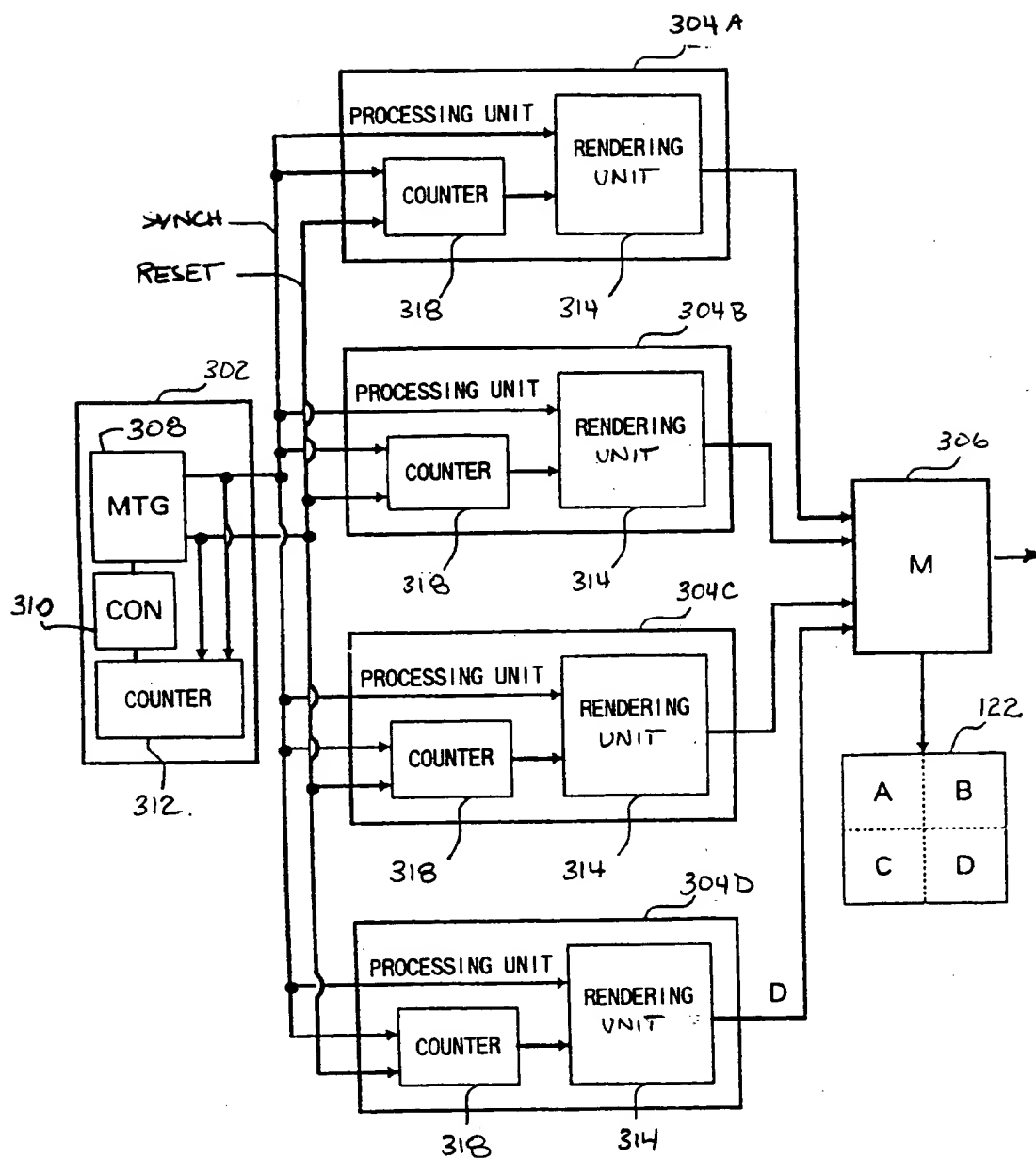
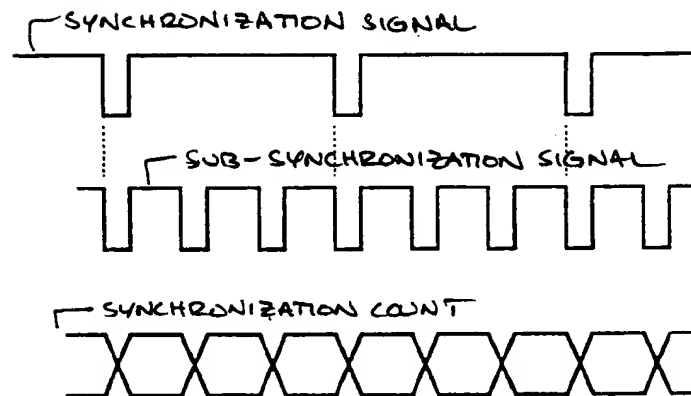
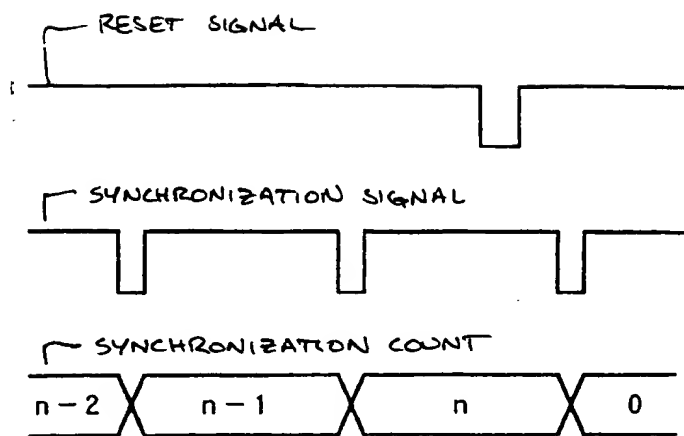
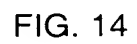


Fig. 12





360

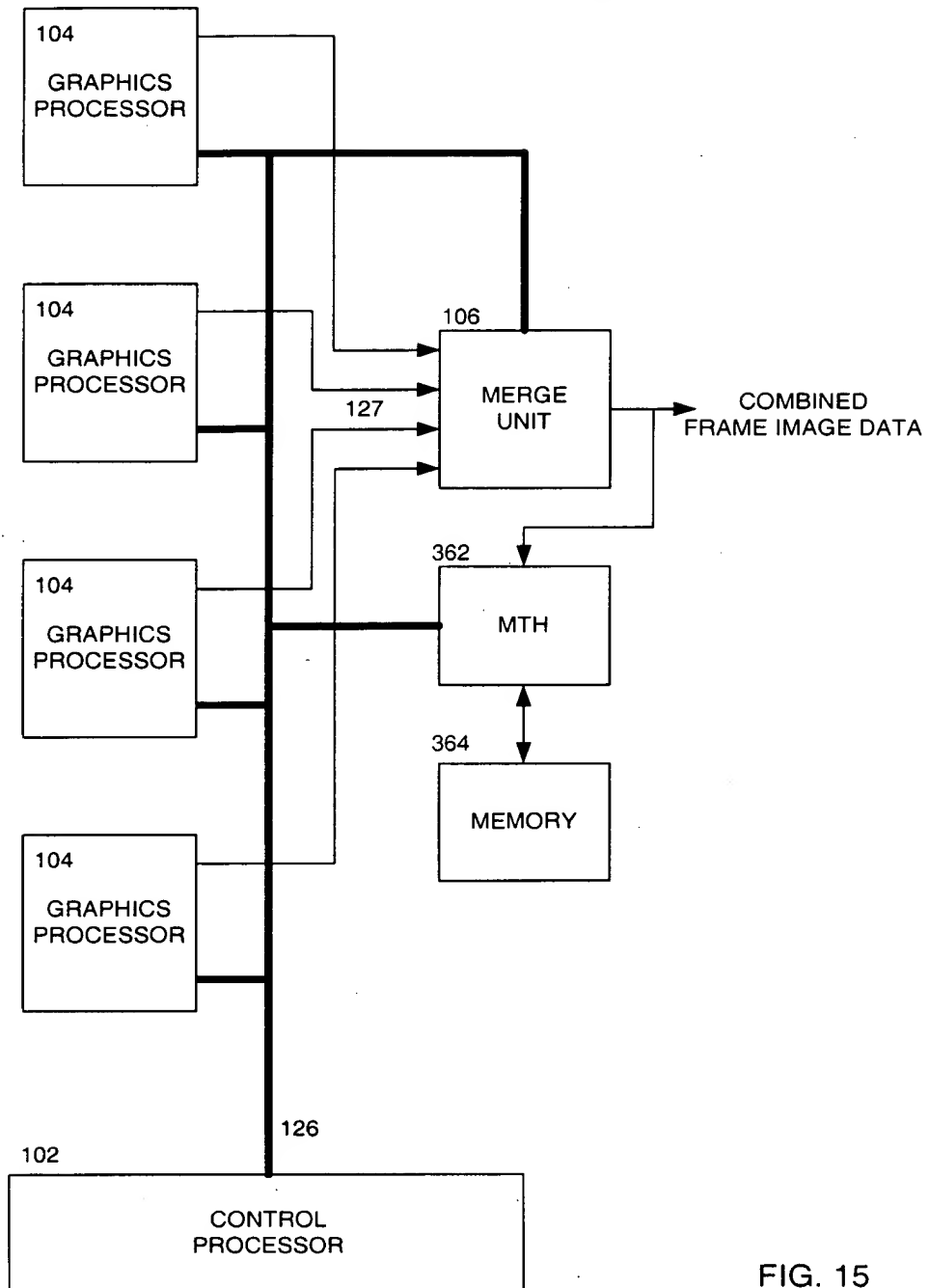


FIG. 15

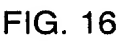


FIG. 16

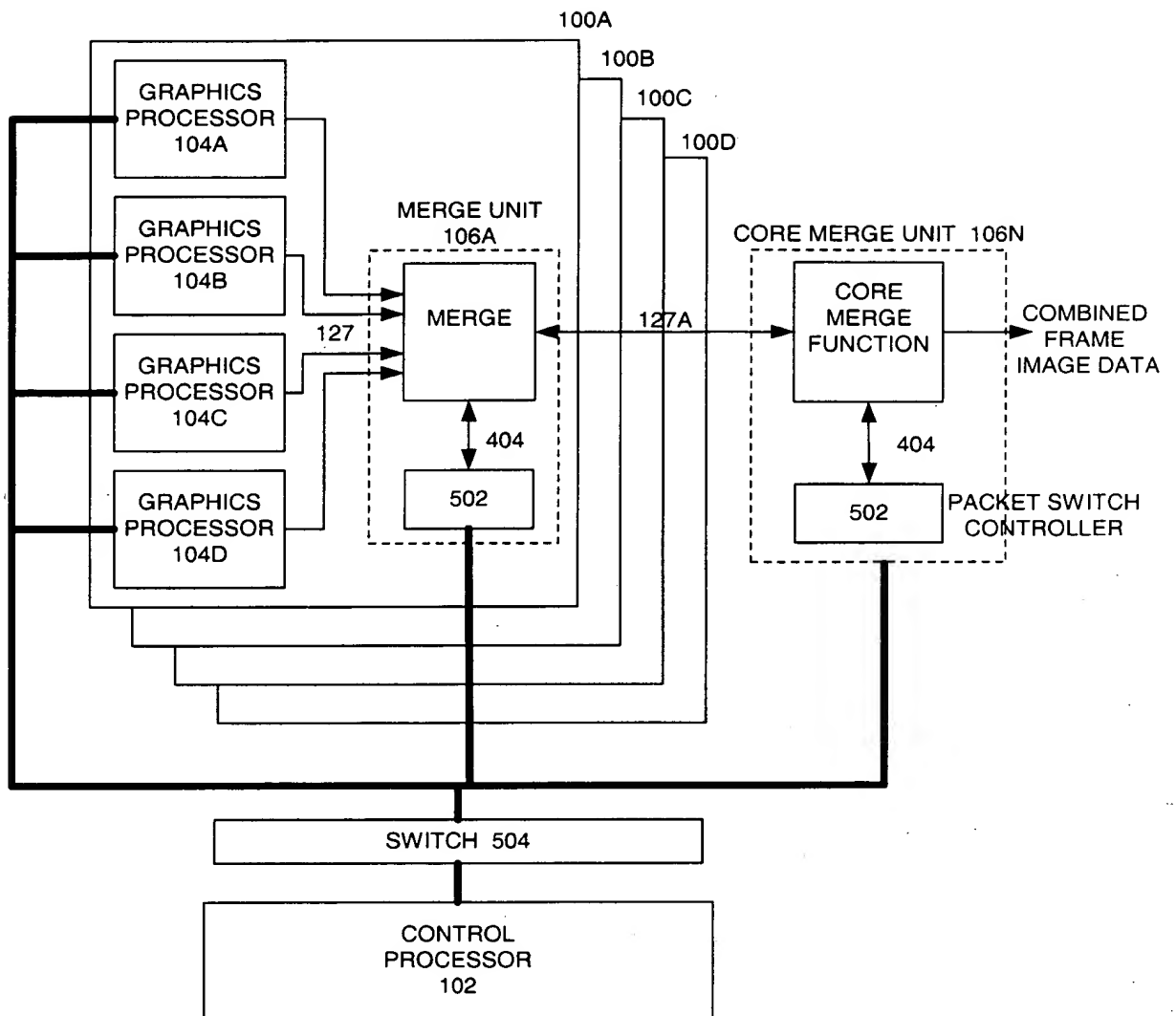


FIG. 17

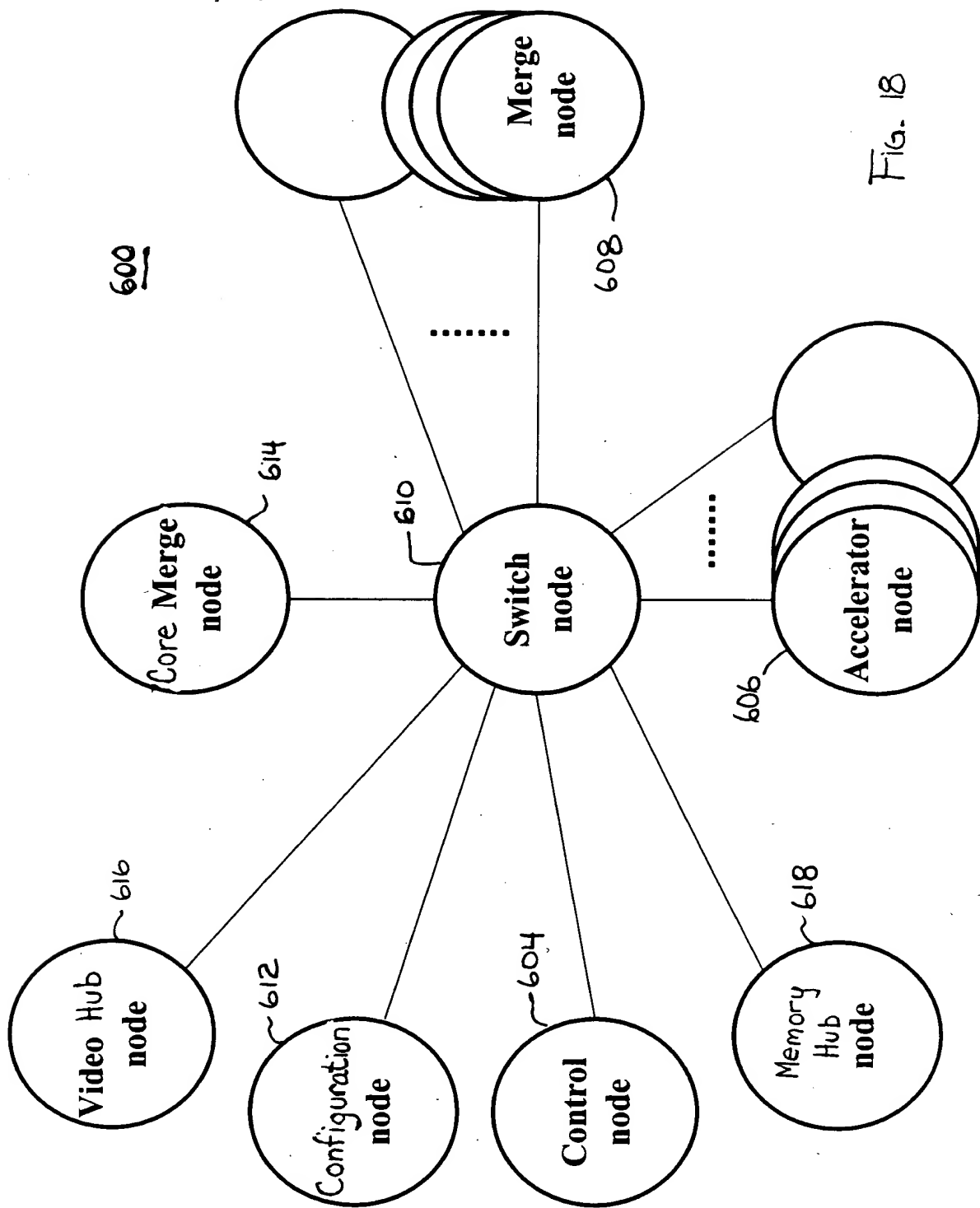


Fig. 18

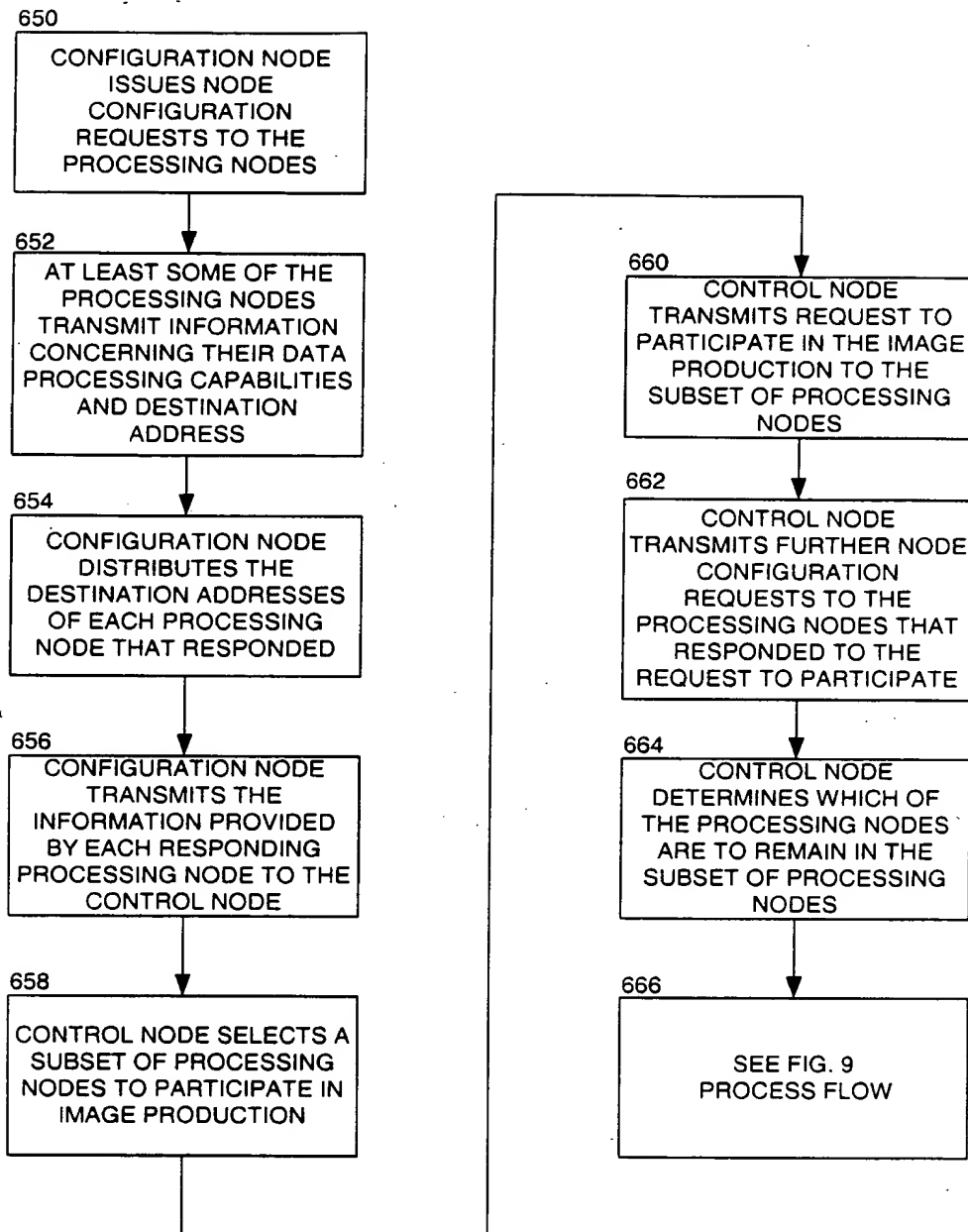


FIG. 19

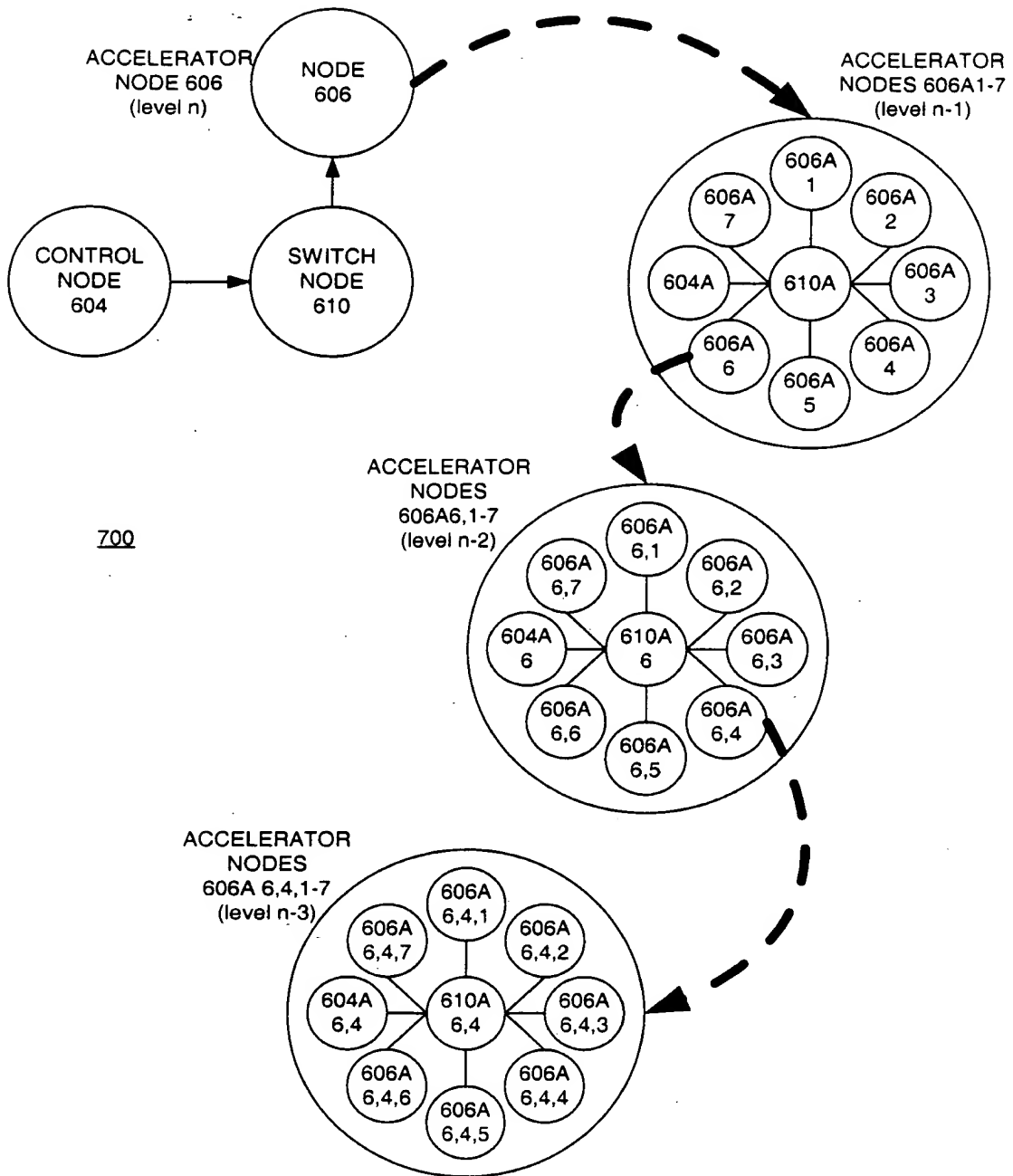


FIG. 20

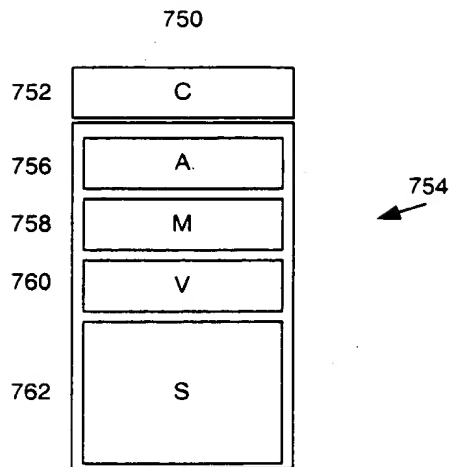


FIG. 21A

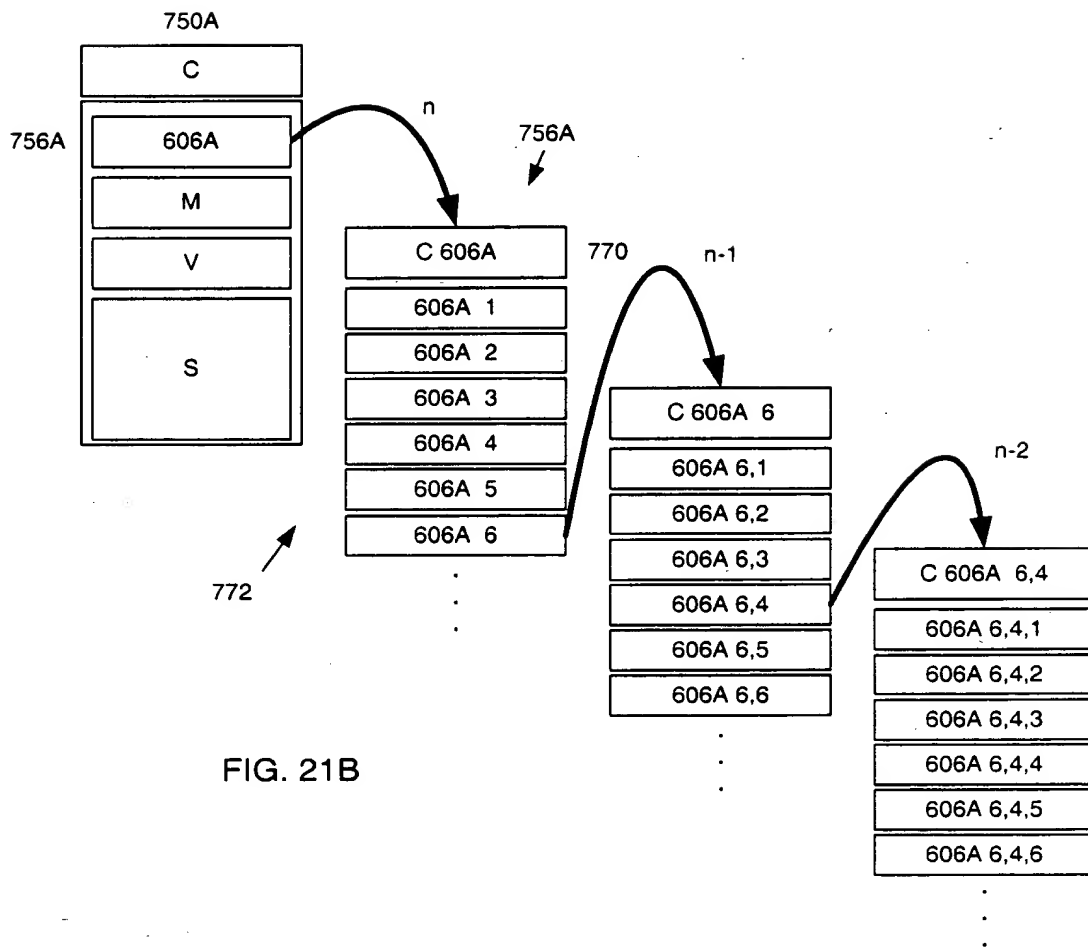


FIG. 21B